Matter No.: 14225-032001 Page Applicant(s): Yusuke Igarashi et al. CIRCUIT DEVICE AND METHOD OF MANUFACTURE THEREOF

1/12



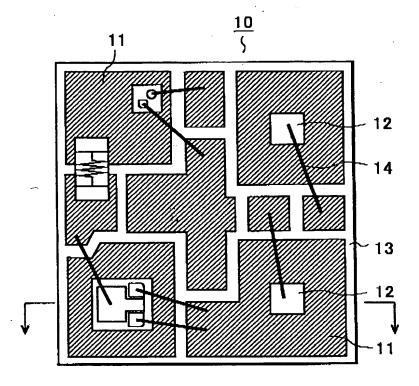


FIG.1B

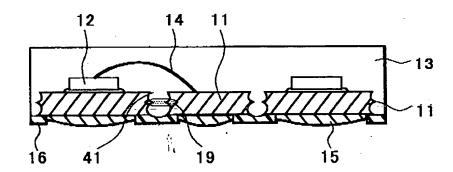
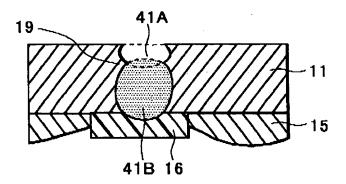
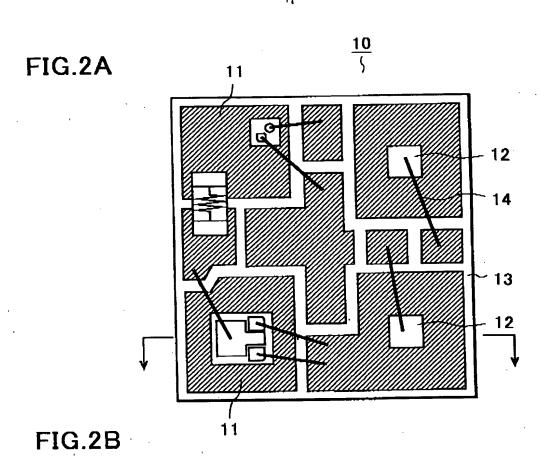


FIG.1C



**THEREOF** 



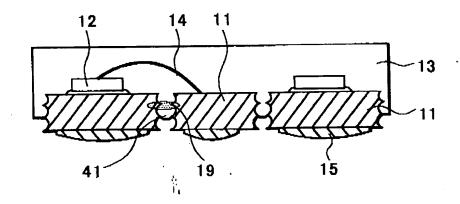


FIG.3

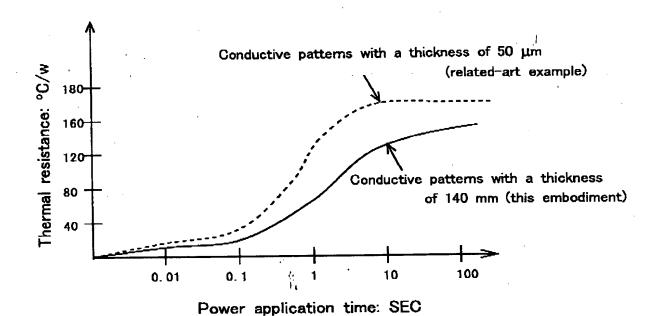


FIG.4A

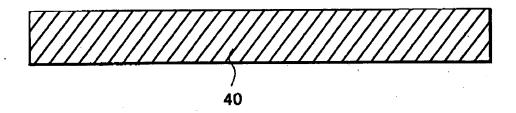
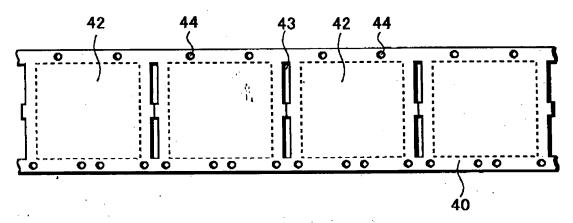


FIG.4B



Page 5 of 12

Matter No.: 14225-032001 Page Applicant(s): Yusuke Igarashi et al. CIRCUIT DEVICE AND METHOD OF MANUFACTURE

**THEREOF** 

5/12

FIG.5A

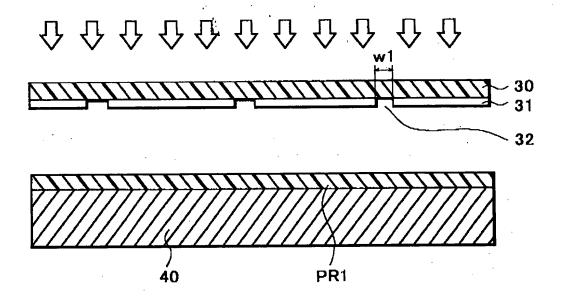
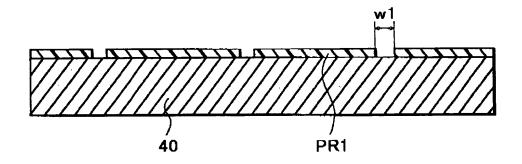


FIG.5B



Matter No.: 14225-032001 Page (Applicant(s): Yusuke Igarashi et al. CIRCUIT DEVICE AND METHOD OF MANUFACTURE THEREOF

FIG.6A

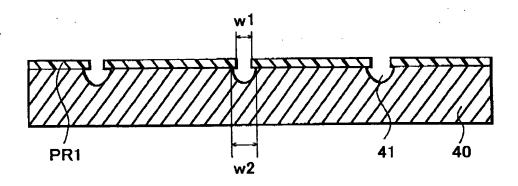
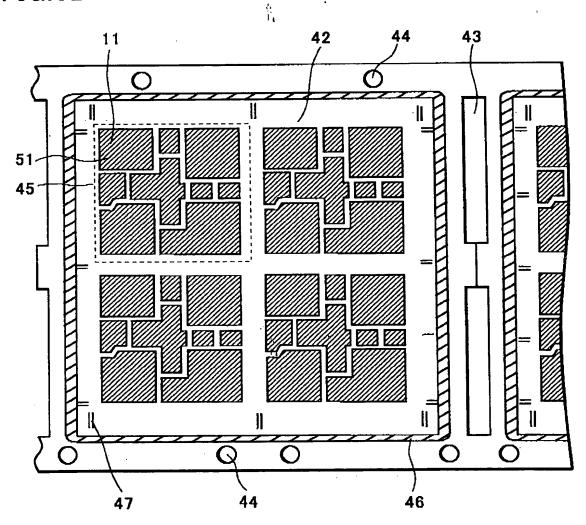


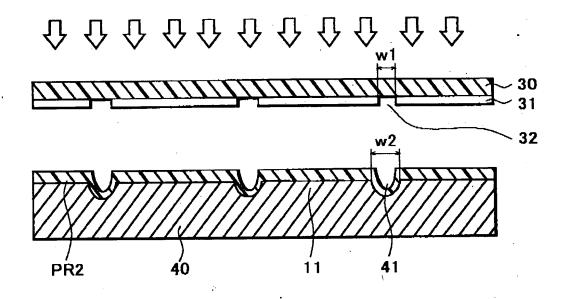
FIG.6B



Applicant(s): Yusuke Igarashi et al.
CIRCUIT DEVICE AND METHOD OF MANUFACTURE

THEREOF

FIG.7A





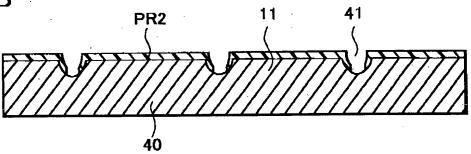
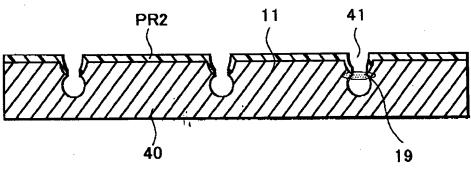


FIG.7C



Page 8 of 12

Matter No.. 14225-032001 Page 8
Applicant(s): Yusuke Igarashi et al.
CIRCUIT DEVICE AND METHOD OF MANUFACTURE
THEREOF

FIG.8A

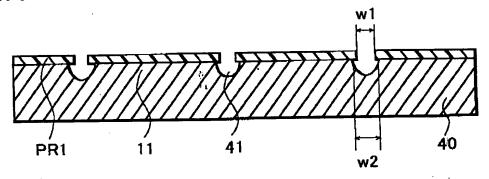


FIG.8B

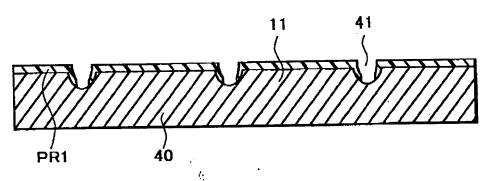
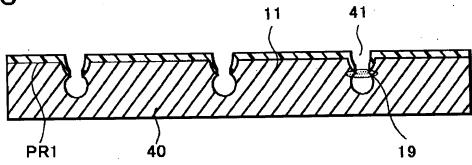


FIG.8C



Page 9 o. . ∠

Matter No.: 14225-032001 Page 9
Applicant(s): Yusuke Igarashi et al.
CIRCUIT DEVICE AND METHOD OF MANUFACTURE
THEREOF

FIG.9A

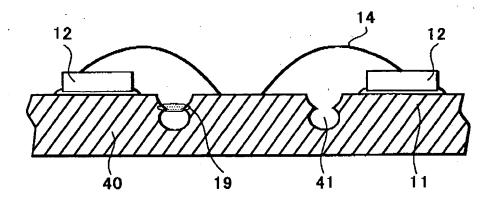
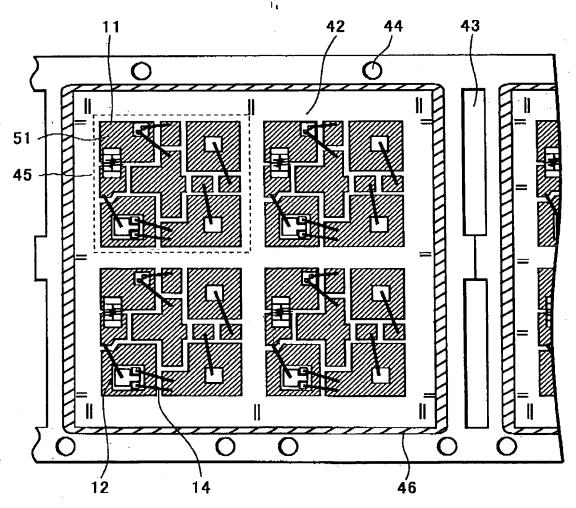


FIG.9B



Page 10 of 12

Matter No.: 14225-032001 Page 16
Applicant(s): Yusuke Igarashi et al.
CIRCUIT DEVICE AND METHOD OF MANUFACTURE
THEREOF

10/12

FIG.10

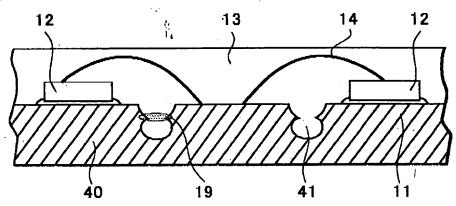


FIG.11

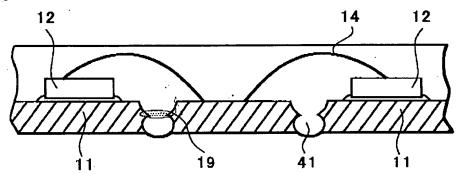
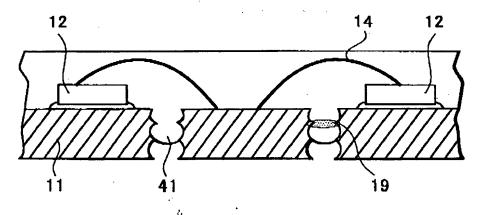
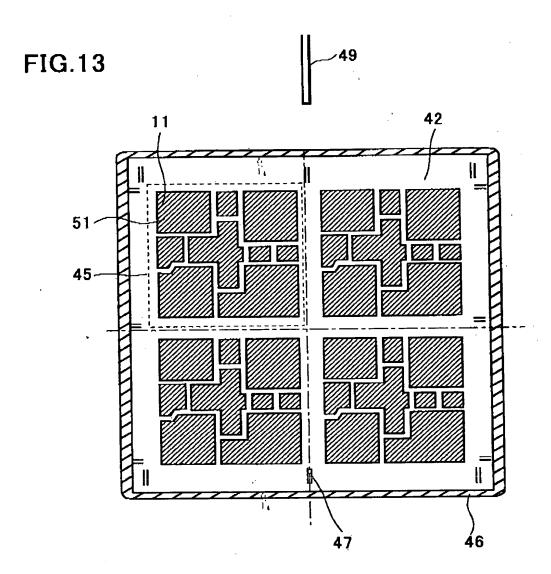


FIG.12



Page 11 of 12

Matter No.: 14225-032001 Page 11 Applicant(s): Yusuke Igarashi et al. CIRCUIT DEVICE AND METHOD OF MANUFACTURE THEREOF



Matter No.: 14225-032001 Page 1 Applicant(s): Yusuke Igarashi et al. CIRCUIT DEVICE AND METHOD OF MANUFACTURE THEREOF

12/12 ()

FIG.14

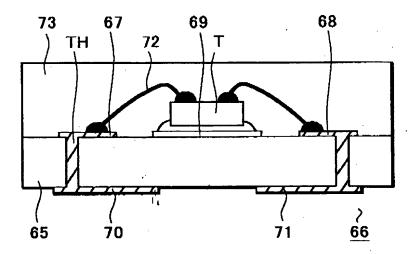


FIG.15

